

Abstract Submitted  
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**Module Production for the UT<sup>1</sup>** HANGYI WU, Syracuse University —  
The LHCb upgrade is based on all detectors being able to output data in real time at a 40 MHz rate. This requires replacing sensors and kapton hybrid circuits containing the SALT electronic chips that processes the data from the sensors. Here I explain that the hybrid currents have ASICs mounted on them in Milano and shipped to Syracuse where the hybrids and silicon sensors are assembled, wire-bonded, potted and tested.

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