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Error Corrected Quantum Annealing with Hundreds of Qubits

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Physical realizations of quantum computing are always threatened by decoherence, especially as the scale of the device increases. This makes the implementation of error correction crucial. We have developed error correction techniques tailored to quantum annealing using superconducting adiabatic quantum optimization processors. I will show experimental results for computations using up to 448 physical qubits on the D-Wave Two device. Scaling of code performance on both antiferromagnetic chains and random 2D Ising problems will be addressed, along with insights into device error mechanisms and choices of decoding strategy. The error correction substantially enhances the observed success probabilities.