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Grid-bus surface code architecture SIMON NIGG, University of Basel, ANDREAS FUHRER, IBM research Zurich, DANIEL LOSS, University of Basel — We present a scalable hybrid architecture for the 2D surface code combining superconducting resonators and spin qubits in nanowires with tunable spin-orbit coupling. The back-bone of this architecture is a square lattice of capacitively coupled coplanar waveguide resonators each of which hosts a nanowire spin-orbit qubit. A simple circuit QED model is derived for the coupling between the spin degree of freedom and the quantized resonator modes on the lattice. The electrically tunable qubit frequency allows for fast single qubit phase gates. A two-qubit \sqrt{iSWAP} gate between neighboring qubits can be realized by a third order process, whereby a virtual photon in one cavity is created by a first qubit, coherently transferred to a neighboring cavity, and absorbed by a second qubit in that cavity. Numerical simulations with realistic parameters predict high gate fidelities.

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