

Abstract Submitted
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LArPix-v2: a commercially scalable large-format 3D charge-readout scheme for LArTPCs BROOKE RUSSELL, Lawrence Berkeley National Laboratory — 3D ionization information facilitates unambiguous mm-scale fine-tracking in high occupancy liquid argon time-projection chamber (LArTPC) environments. LArPix-v2 incorporates low-power 64-channel custom ASICs with a mixed-signal large-format printed circuit board for an unambiguous 3D charge-readout anode. With robust I/O and control architecture, a 10-by-10 array of ASICs instrument a 4,900-pixel PCB-based anode. The system is compatible with standard large-scale commercial electronics production techniques, enabling low-cost quick-turn production. Here I present a system design overview alongside LArPix-v2 ASIC and pixel anode tile benchmark performance evaluation. This system will be deployed in upcoming ProtoDUNE-ND LArTPC physics operation.

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