

Abstract Submitted  
for the DFD06 Meeting of  
The American Physical Society

**Microfluidic bubble logic**<sup>1</sup> MANU PRAKASH, NEIL GERSHENFELD,  
MIT — We present a new all-fluidic logic family based on two-phase flow in micro-  
scale geometries. Hydrodynamic interactions are exploited as a primary mechanism  
to introduce nonlinearity. Presence or absence of a bubble represents a bit. A bubble  
can thus carry both information and a material payload at the same time. Microflu-  
idic bubble logic gates (AND/OR/NOT), memory and cascaded boolean circuits  
will be presented. Applications of such a control scheme to large-scale integrated  
bio-chemical processors will be highlighted.

<sup>1</sup>Supported by CBA (NSF grant CCR0122419).

Manu Prakash  
MIT

Date submitted: 04 Aug 2006

Electronic form version 1.4