

Abstract Submitted  
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***P*-type InSb and In<sub>x</sub>Ga<sub>1-x</sub>As quantum wells remotely doped with Be** CHOMANI GASPE, MADHAVIE EDIRISOORIYA, TETSUYA MISHIMA, MICHAEL SANTOS, University Of Oklahoma — CMOS circuits require *p*-type transistors with high hole mobility, in addition to *n*type transistors with high electron mobility. We have observed room-temperature hole mobilities of 100 and 600 cm<sup>2</sup>/Vs in In<sub>x</sub>Ga<sub>1-x</sub>As and InSb quantum wells, respectively. The In<sub>x</sub>Ga<sub>1-x</sub>As wells are remotely doped with Be in In<sub>x</sub>Al<sub>1-x</sub>As barrier layers, and grown on InP substrates. The InSb wells are remotely doped with Be in Al<sub>x</sub>In<sub>1-x</sub>Sb barrier layers, and grown on GaAs substrates. We will discuss the effects of strain, structural parameters, and defect density on hole mobility in InSb and In<sub>x</sub>Ga<sub>1-x</sub>As quantum wells. In zinc-blende semiconductors, a narrower band gap leads to smaller effective masses for electrons and holes. Strain and confinement increase the energy splitting between holes with light in-plane mass and those with heavy in-plane mass. This work was supported by the NSF Grants DMR-0808086 and DMR-0520550.

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