

Abstract Submitted  
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**Nanoscale control at the LaAlO<sub>3</sub>/SrTiO<sub>3</sub> Interface grown on LSAT**<sup>1</sup> DANIELA BOGORIN<sup>2</sup>, CHENG CEN, Department of Physics and Astronomy, University of Pittsburgh, Pittsburgh, PA 15260, CHUNG WUNG BARK, CHANG BEOM EOM, Department of Materials Science, University of Wisconsin-Madison, Madison, WI 53706, JEREMY LEVY, Department of Physics and Astronomy, University of Pittsburgh, Pittsburgh, PA 15260 — The two-dimensional electron gas (2DEG) that forms at the interface between two semiconductors or between a semiconductor and oxide is currently the basis for some of the most useful electronic devices. We are able to control the 2DEG interface between LaAlO<sub>3</sub>/SrTiO<sub>3</sub> with nanoscale precision and create transistors, nanodiodes and other nanostructures. Future scaling of oxide nanoelectronics requires scaling to wafer sizes larger than what can be provided from SrTiO<sub>3</sub>. (LaAlO<sub>3</sub>)<sub>0.3</sub>–(Sr<sub>2</sub>AlTaO<sub>3</sub>)<sub>0.7</sub> (LSAT) substrates can allow for coherently strained LaAlO<sub>3</sub>/SrTiO<sub>3</sub> heterostructures to be created. A sharp insulator to metal transition occurs at 8 uc LaAlO<sub>3</sub> thicknesses, in contrast to what is observed for unstrained SrTiO<sub>3</sub> substrates. We describe the properties of nanoscale structures created at the 2DEG interface of LaAlO<sub>3</sub>/SrTiO<sub>3</sub> grown on LSAT wafers and compare them with structures grown on bulk SrTiO<sub>3</sub> substrates.

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